ABESIT

Cadence Design Contest 13th Edition

Cadence India Invites all engineering graduate and undergraduate students to participate in 13th Edition of the Cadence Design Contest. Students are to submit an abstract on any digital, analog or board design based on guidelines before **August 20, 2018.**

Categories

Prizes will be given in two categories. One winner and one runner-up will be chosen in *each* of these category:

- Full-time BE/B.Tech students
- Full-time ME/M.Tech students

Design Requirements

Submissions for both categories should be in analog, digital, or board design and can be in either of the following two design areas:

- Operational chip design — Designs have been implemented and tested
- Conceptual design — Designs need not have been implemented but must have been thoroughly simulated and must include a test plan

Evaluation Criteria

Submissions are judged by an expert committee, comprised of representatives from the industry and Cadence experts, on pre-defined criteria including:

- Inventiveness
- Complexity and feasibility of project
- Breadth of design
- Effective use of Cadence technologies

Prize

- Each winning team member and guide will receive an Apple Watch 3
- Each runner-up team member and guide will receive a Google Home
- Each finalist team member and guide will receive a certificate
- All finalists will be given preference for the paid internship program at Cadence
- Each member of a teams clearing the first round of the contest will receive a Cadence Design Contest 2018 branded cap.

For details log on to https://www.cadence.com/content/cadence-www/global/en_US/home/services/cadence-academic-network/university-software-program/design-contests/cadence-design-contest-2018.html
Last date: 20 August 2018

Notice Boards   (**Prof. S.B.Bajpayee**)

Hostels   **Head –CEI & Proctor**

CC to:

1. Hon'ble Chairman Sir (e-mail)
2. Hon'ble Advisor Sir
3. Director, ABESIT(e-mail)
4. Prof. (Dr.) S. S. Chauhan, Deputy Director & Dean (e-mail)
5. All HOD/In-charges (e-mail)