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Vice Chancellor
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U.P. , Lucknow

Shri Ashu Goel
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ABES Institute of Technology
Ghaziabad

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Our Centers of Excellence in the Campus

Automation Technologies
Hydraulic System
Rexroth
Bosch Group

Power Electronics & Drives
a seat of AKTU
with **ENTUPLE** TECHNOLOGIES

Analog & Embedded Systems
with
TEXAS INSTRUMENTS

Industrial Automation
Pneumatic Systems
FESTO

Advanced Robotic Control
for Industrial Automation
with
Indo Euro Synchronization
EUROPÄISCHES ZENTRUM FÜR MECHATRONIK

Remote Sensing and Geographic
Information System

Entuple Technologies
ANSYS Mechanical
ANSYS

VLSI in Embedded Systems
& Robotics
e-yantra
of IIT Bombay

Big Data
with CEO
dalreon

Internet of Things (IoT), Entuple Technologies &
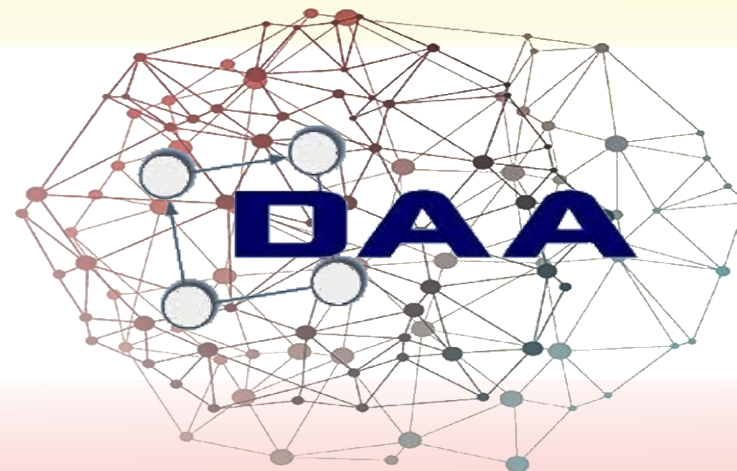**C-DAC, Bangaluru**
ENTUPLE TECHNOLOGIES

Technical Education Quality Improvement Programme-Phase III (TEQIP-III)

Faculty Development Program

on

Design and Analysis of Algorithm

11th - 15th June, 2019

DAA

Supported & Sponsored by

Dr. APJ Abdul Kalam Technical University

Lucknow, Uttar Pradesh

and

Organized by

Department of Computer Science & Engineering

ABESIT

About the Institute

The ABES Institute of Technology (AKTU College Code 290) embarked on its mission in the field of Technical Education in the year 2007. It is situated on a prominent location at National Highway-24, near Delhi-Ghaziabad Border. The Institute campus is decent, green, clean with world class infrastructure for which it is awarded consecutively 7th Rank in all India, 1st Rank among Technical Institution in North India for Swachh Campus Ranking 2017 and 6th Rank in all India for Swachh Campus Ranking 2018 by AICTE/MHRD. Govt. of India. The teaching faculty of the institute is qualified, competent and painstaking. While teaching University Curriculum, emphasis is also laid on conducting co-curricular and extra-curricular activities regularly to ensure the overall development of the students. Recently, Institute has been selected for establishing Institution Innovation Council (IIC) as per the norms of Innovation Cell, Ministry of HRD, Govt. of India. The Institute has 10 Centers of Excellence to hone the skill of students and faculty members in different areas of technologies. Career Resource Center (CRC) of ABESIT plays a pivotal role in conducting placement drives and enhancing the capabilities and competencies of students.

Institute Vision

To be a leading institution in technical education providing education & training enabling human resource to serve nation & world at par with global standards in education.

Institute Mission

- Developing state-of-the art Infrastructure which also includes establishment of Center of Excellence in pursuit of Academic & Technical Excellence.
- Valuing workforce, inculcating belongingness & Professional Integrity.
- To develop human resource to solve local, regional & global problems to make Technology relevant to those who need it most.

Department of CSE: Vision

To provide excellence by imparting knowledge to the learners enabling them to become skilled professionals to be recognized as a responsible citizen.

Department of CSE: Mission

- Provide quality education in the field of computer science and engineering through experienced and qualified faculty members.
- Motivate learners for higher studies and research oriented activities by utilizing resources of Centers of Excellence.
- Inculcate societal values, professional ethics, team work, and leadership qualities by having exposure at National and International level activities.

Objective of FDP

The objective of this programme is to provide an understanding of the fundamentals and a comprehensive knowledge in the field of Computer Algorithms. At the end of FDP, your knowledge of algorithm design, latest practices and problem solving techniques will be refreshed. The approach demonstrated will draw your students to take interest in algorithmic problem solving and help them for facing placement interviews and to work over different work area.

Upon completion of this FDP, we will be able to do the following.

- Analyze the asymptotic performance of algorithms.
- Write correctness proofs for algorithms.
- Demonstrate a familiarity with major algorithms and data structures.
- Apply important algorithmic design paradigms and methods of analysis
- Synthesize efficient algorithms in common engineering design situations.

Topics to be deliberated

Area to be covered include, but not limited to:
- **Algorithmic Problem Solving**
- **Framework for Algorithm Analysis**
- **Genetic Algorithm**
- **Security Algorithm**
- **Machine Learning Algorithm**

Who can participate

The program is open for faculty members, M.Tech students, Research Scholars and Industry Professionals.

Registration (Only 30 Participants on the basis of FCFS)

Registration fee : Nil
For Registration follow the URL: **tiny.cc/ABESIT_FDP**

Certificate

Participation Certificates shall be awarded to all the participants on the concluding day.

Resource Persons

1. Dr. Yashwant Prasad Singh
   Former Professor,
   Multimedia University,
   Malaysia

2. Dr. Manish Gaur
   Professor, IET Lucknow

3. Dr. Mohd Amjad
   Associate Professor, Jamia Millia Islamia

4. Dr. Vijay Dhir
   Director R&D,
   Sant Baba Bhag singh University

5. Dr. R. Ramakishore
   Associate Professor, IP University

6. Dr. Pradeep Kumar
   Associate Professor & Head
   Maulana Azad National Urdu University,
   Hydrabad

7. Dr. Parmanand
   Dean, School of Engineering and
   Technology, Sharda University, G.B. Nagar

8. Mr. Dinesh Kumar
   Scientist, STQC Directorate
   Ministry of Electronic & Information
   Technology, Goa

9. Mr. Vivek Kumar Gupta
   Project Engineer, CRIS (Ministry of
   Railways, Govt of India), New Delhi

10. Mr. Piyush Kulshreshtha
    Former Director, Alcatel-Lucent

11. Mr. Manish Sharma
    Software Architect, Lenskart, Noida

12. Mr. Pankaj Kumar Pathak
    Senior Big Data Consultant, HCL Noida

For more Information please contact

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